

IEEE Workshop on Signal and Power Integrity (SPI) stands today as a premier forum of exchange on all aspects of Signal and Power Integrity, encompassing the latest research and developments on design, characterization, modeling, simulation and testing at chip, package, board, and system levels.

The workshop brings together researchers and developers from academia and industry worldwide, providing a key environment to foster cooperation and knowledge exchange.

SPI 2026 AWARDS & GRANTS

Best Paper Award | Best Student Paper Award | IEEE EPS Student Travel Grants

CALL FOR PAPERS

Original and unpublished contributions on all aspects of Signal and Power Integrity are welcomed, related to the following TOPICS of interest:

- Modeling and simulation for SI/PI
- Coupled signal and power integrity analysis
- Noise reduction and equalization techniques
- High-speed link design and modeling
- Power distribution networks
- •RF/microwave/mm-wave systems and packaging solutions
- Antennas-in-package and antennas-on-chip
- •3D IC and packages (TSV/SiP/SoC)
- Nano-interconnects and nano-structures
- •Electromagnetic theory and modeling

- Transmission line theory and modeling
- Macromodeling, reduced order models
- Electromagnetic compatibility
- Design methodology/flow measurements
- Jitter and noise modeling
- Stochastic/sensitivity analysis
- Electro-thermal modeling
- Chip-package co-design
- Novel CAD concepts Optical interconnects
- Al in electronics design

Full Paper Submission January 16, 2026 Notification of Acceptance February 11, 2026 Final Paper Submission March 27, 2026 Early Bird Registration March 27, 2026 **Awards & Grants Application** March 27, 2026

Sponsors Registration March 31, 2026

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